

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A driving device of a flat display panel comprising:

a scan driving circuit for simultaneously applying scan pulses to both ends of each scan line of the flat display panel;

a first data driving circuit for applying data pulses to odd numbered data lines among data lines of the flat display panel; and

a second data driving circuit for applying data pulses to even numbered data lines of the data lines, wherein the scan pulses have a same voltage, a same phase and a same pulse width.
2. (Previously Presented) The device of claim 1, further comprising a controller for outputting a control signal for controlling the scan driving circuit.
3. (Original) The device of claim 2, wherein the controller outputs a control signal for simultaneously applying the scan pulses.
4. (Canceled)

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5. (Currently Amended) A driving device of a flat display panel comprising:

a first scan driving circuit for applying scan pulses to one side of each scan line of the flat display panel;

a second scan driving circuit for applying the scan pulses to the other side of each scan line;

a first data driving circuit for applying data pulses to odd numbered data lines among data lines of the flat display panel; and

a second data driving circuit for applying data pulses to even numbered data lines of the data lines, wherein the scan pulses have a same voltage, a same phase and a same pulse width.

6. (Previously Presented) The device of claim 5, further comprising a controller for outputting a control signal to control the first and second scan driving circuits.

7. (Original) The device of claim 6, wherein the controller outputs a control signal to simultaneously apply the scan pulses.

8-10 (Canceled)

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11. (Currently Amended) A driving method of a flat display panel comprising:
simultaneously applying scan pulses to both ends of each scan line of the flat display panel;
applying data pulses to odd numbered data lines among data lines of the flat display panel; and
applying data pulses to even numbered data lines of the data lines, wherein the scan pulses have a same voltage, a same phase and a same pulse width.

12. (Original) The method of claim 11, further comprising outputting a control signal to simultaneously apply the scan pulses.

13. (Canceled)

14. (Currently Amended) The method of claim ~~[[1]]~~11, wherein applying data pulses to odd numbered data lines includes a first data driving unit applying data pulses to the odd numbered data lines.

15. (Currently Amended) The method of claim 14, wherein applying data pulses to even numbered data lines includes a second data driving unit applying data pulses to the even numbered data lines.

16. (Previously Presented) The device of claim 1, wherein the odd numbered data lines extend from the first data driving circuit on a first side of the panel to a second side of the panel, and the even numbered data lines extend from the second data driving circuit on the second side of the panel to the first side of the panel.

17. (Previously Presented) The device of claim 16, wherein data pulses for the odd numbered data lines are applied by only the first data driving circuit, and data pulses for the even numbered data lines are applied by only the second data driving circuit.

18. (Previously Presented) The device of claim 1, wherein the first data driving circuit applies data pulses to only odd numbered data lines, and the second data driving circuit applies data pulses to only even numbered data pulses.

19. (Previously Presented) The device of claim 5, wherein the odd numbered data lines extend from the first data driving circuit on a first side of the panel to a second side of the panel, and the even numbered data lines extend from the second data driving circuit on the second side of the panel to the first side of the panel.

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20. (Previously Presented) The device of claim 19, wherein data pulses for the odd numbered data lines are applied by only the first data driving circuit, and data pulses for the even numbered data lines are applied by only the second data driving circuit.